

```

1 /* *****
* Hardware Abstraction Layer Header file for Microchip PIC16F8xx      *
* -----*
5 * Copyright (C) 2004 KOCH Engineering                               *
* Henrik J. Koch                                                    *
* email:  mail@koch-engineering.com                                  *
* web:    http://www.koch-engineering.com                            *
* (MPLAB-IDE 6.43)                                                 *
10 * Version 1.0  Marts 14. 2004                                     *
***** */
#include "receiver.h"

// Peripheral Register (PIE1)
15 #define PARELLEL_SLAVE_PORT_RW_ENABLE    0x80
#define PARELLEL_SLAVE_PORT_RW_DISABLE    0x00
#define AD_CONVERTER_ENABLE                0x40
#define AD_CONVERTER_DIABLE                0x00
#define USART_RECEIVE_ENABLE               0x20
20 #define USART_RECEIVE_DISABLE            0x00
#define USART_TRANSMIT_ENABLE              0x10
#define USART_TRANSMIT_DISABLE             0x10
#define SYNCHRONOUS_SERIAL_PORT_ENABLE     0x08
#define SYNCHRONOUS_SERIAL_PORT_DISABLE   0x00
25 #define CAPTURE_COMPARE_PWM1_ENABLE      0x04
#define CAPTURE_COMPARE_PWM1_DISABLE      0x00
#define TIMER2_TO_PR2_MATCH_ENABLE         0x02
#define TIMER2_TO_PR2_MATCH_DISABLE       0x00
#define TIMER1_OVERFLOW_ENABLE             0x01
30 #define TIMER1_OVERFLOW_DISABLE          0x00

// INTCON Register
#define GLOBAL_INTERRUPT_ENABLE            0x80
#define GLOBAL_INTERRUPT_DISABLE          0x00
35 #define PERIPHERAL_INTERRUPT_ENABLE      0x40
#define PERIPHERAL_INTERRUPT_DISABLE      0x00
// more to come .....HJK

40 // CCPx defines
enum CCPx_mode {CCP_OFF= 0x00,
                CAPTURE_FALLING_EDGE= 0x04,
                CAPTURE_RISING_EDGE= 0x05,
                CAPTURE_4TH_RISING= 0x06,
45                CAPTURE_16TH_RISING= 0x07,
                COMPARE_SET_ON_MATCH = 0x08,
                COMPARE_CLEAR_ON_MATCH= 0x09,
                COMPARE_SW_INTERRUPT= 0x0A,
                COMPARE_SPECIAL_EVENT= 0x0B,
50                PWM= 0x0C};

extern void peripheral_interrupt_setup(BYTE setup);
extern void set_CCP1_mode(enum CCPx_mode setup);
extern void set_CCP2_mode(enum CCPx_mode setup);
55 extern void peripheral_interrupt_enable(void);
extern void peripheral_interrupt_disable(void);
extern void clear_CCP1_interrupt(void);

60 extern void start_timer1(void);
extern void stop_timer1(void);
extern void clear_timer1(void);
extern WORD get_timer1(void);

```