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1  /* *****
   * Hardware Abstraction Layer for Microchip PIC16F8xx          *
   * -----
   * Copyright (C) 2004 KOCH Engineering                        *
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   * (MPLAB-IDE 6.43)                                        *
   * Version 1.0  Marts 14. 2004                              *
10 ***** */
#include    "hal.h"
#include    "PIC.h"
#include    "receiver.h"

15
void peripheral_interrupt_setup(BYTE setup)
{
    PIE1 = setup;
}
20
void set CCP1_mode(enum CCPx_mode setup)
{
    CCP1CON = setup;
}
25
void set CCP2_mode(enum CCPx_mode setup)
{
    CCP2CON = setup;
}
30
void peripheral_interrupt_enable(void)
{
    INTCON |= 0x40;
}
35
void peripheral_interrupt_disable(void)
{
    INTCON &= 0xBF;
}
40
void clear CCP1_interrupt(void)
{
    CCP1IF = 0;
}
45
void start_timer1(void)
{
    TMR1ON = 1;
}
50
void stop_timer1(void)
{
    TMR1ON = 0;
}
55
void clear_timer1(void)
{
    TMR1L = 0;
    TMR1H = 0;
60 }

WORD get_timer1(void)
{
    // return BYTE counter registers to one WORD
65     return ((256*TMR1H)+TMR1L);
}

70

```